

FIG. 1

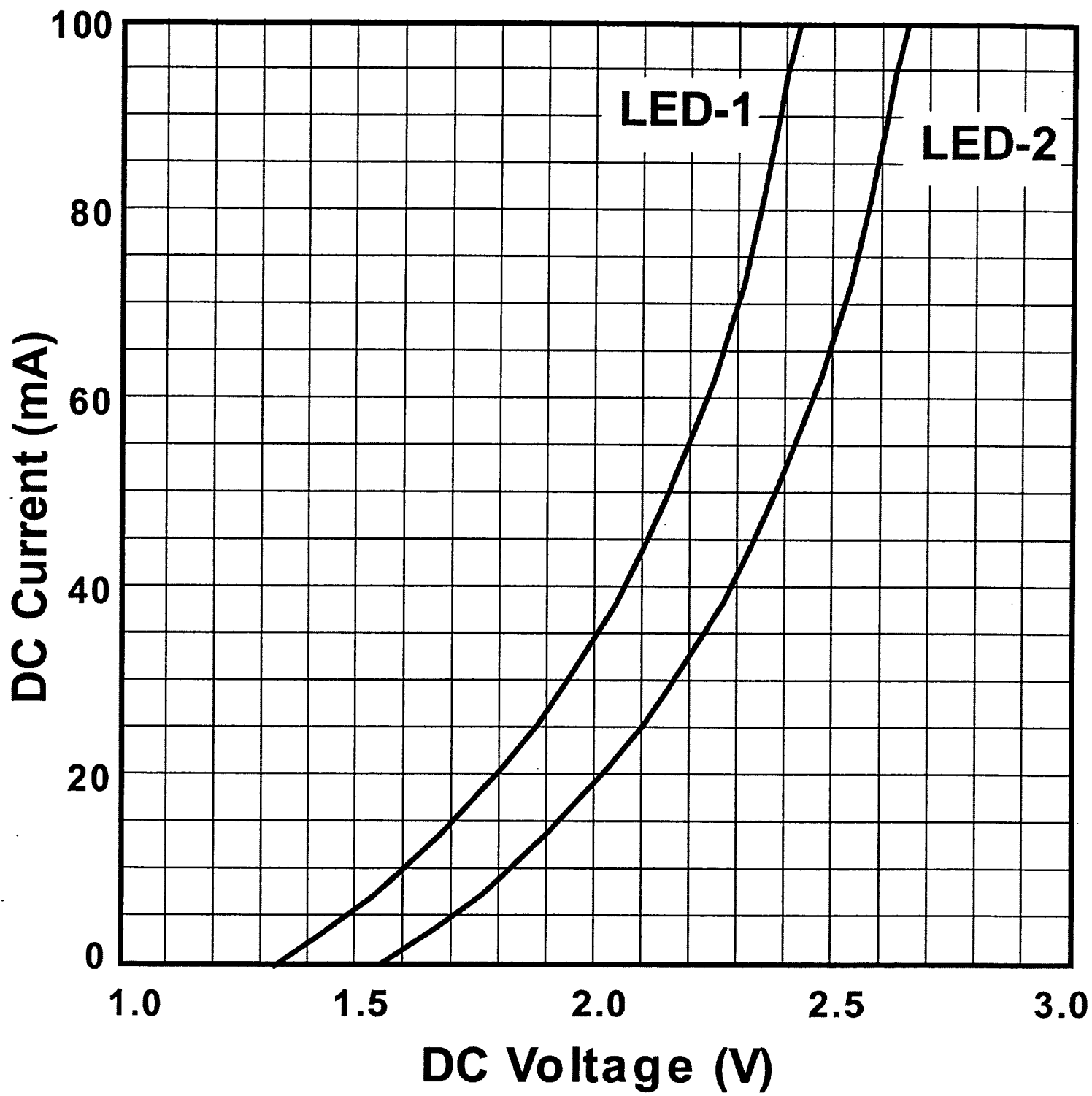


FIG. 2

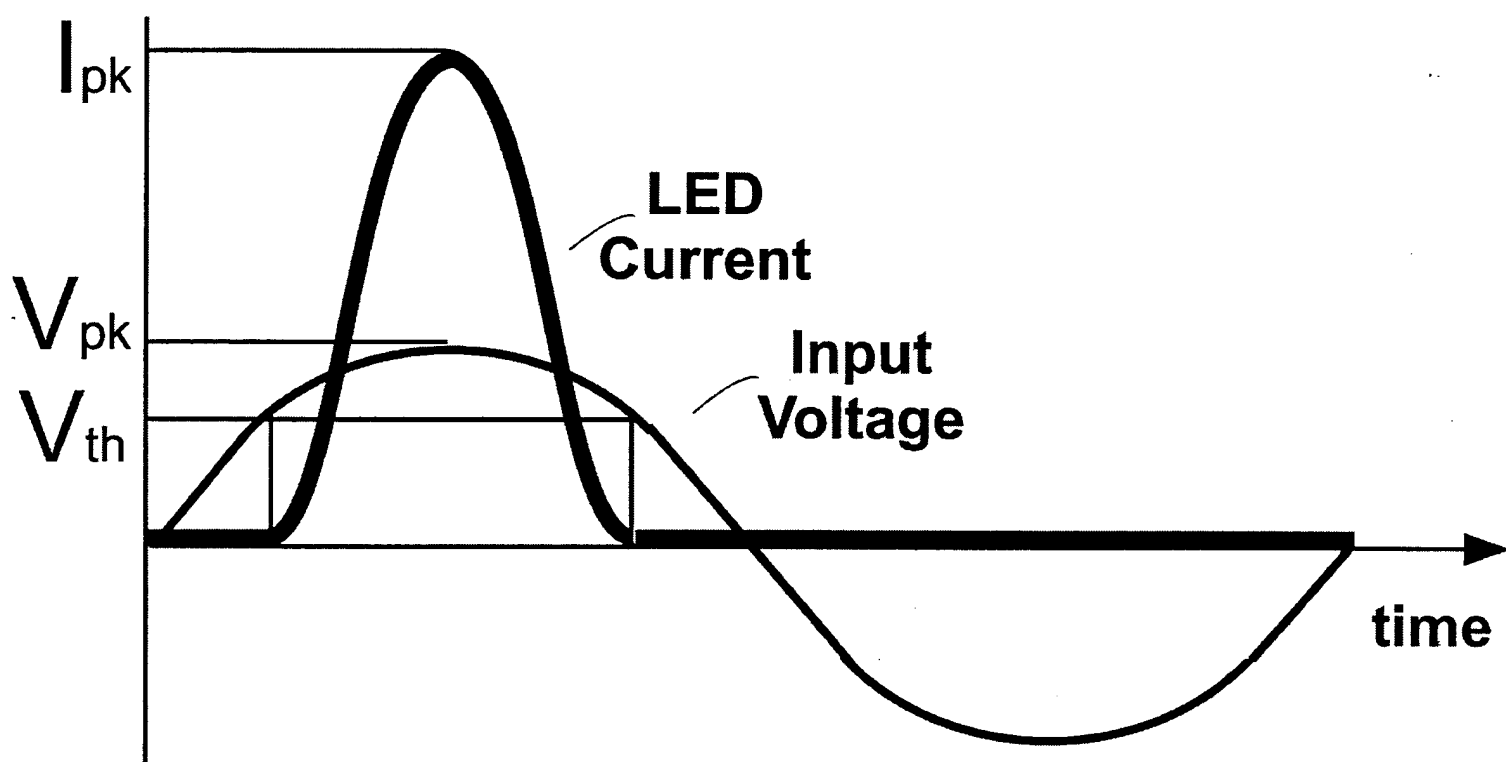


FIG. 3

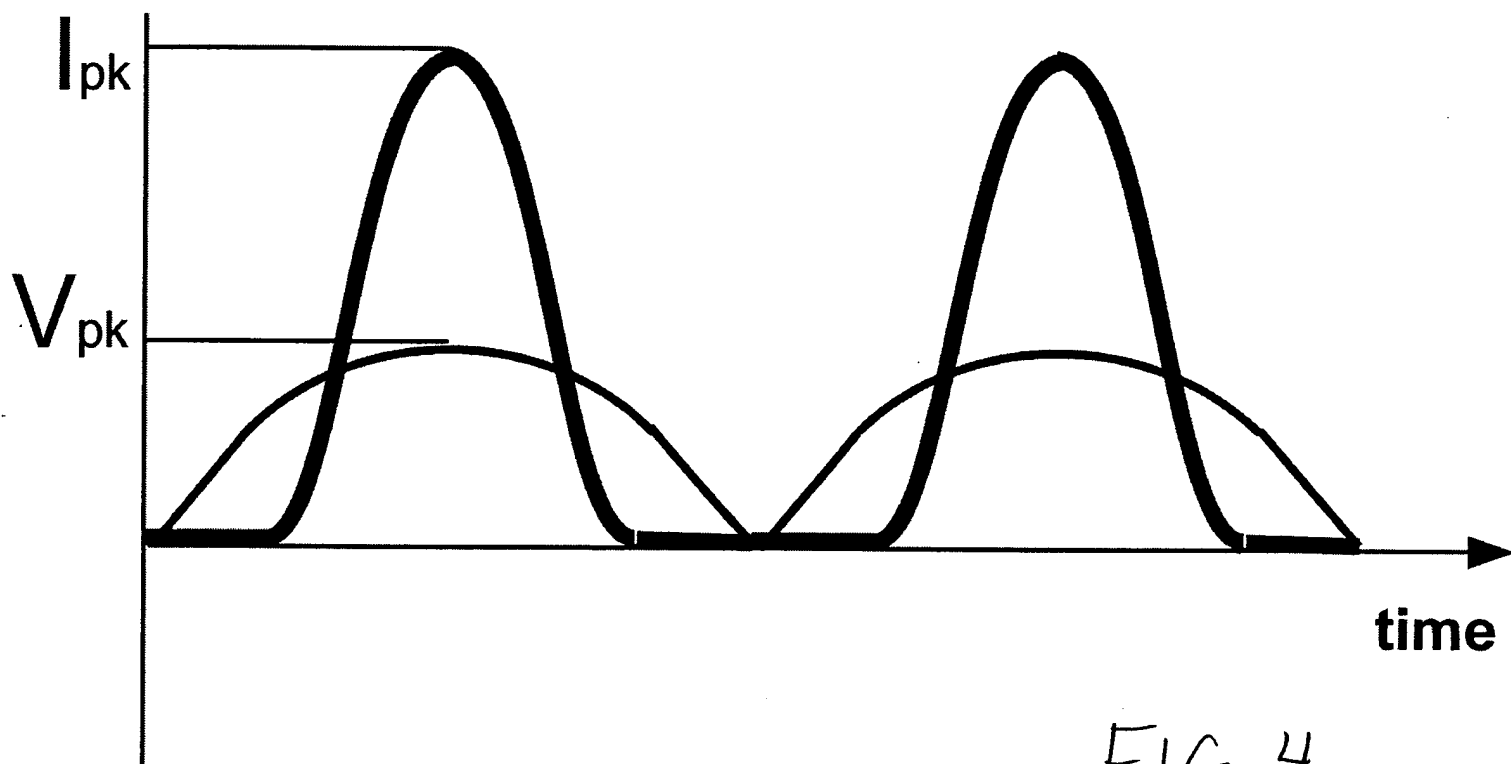


FIG. 4

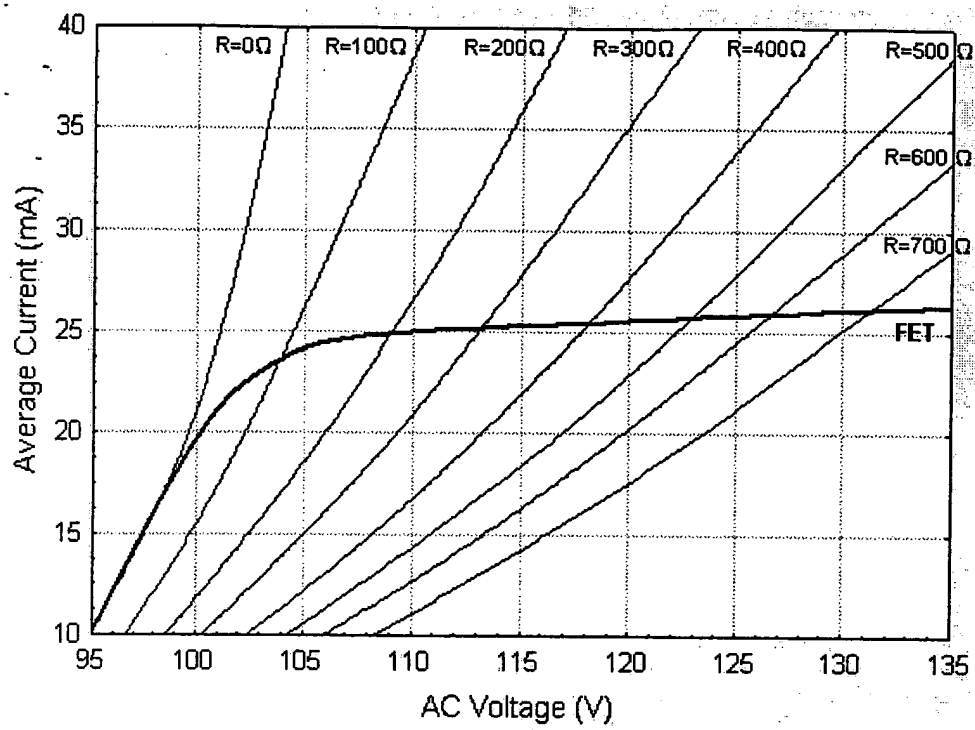
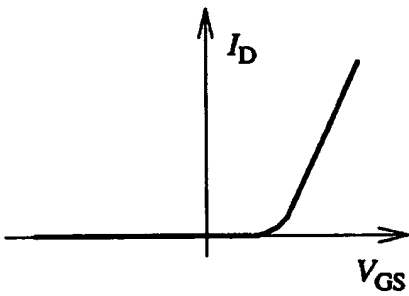
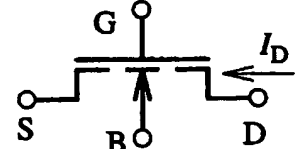
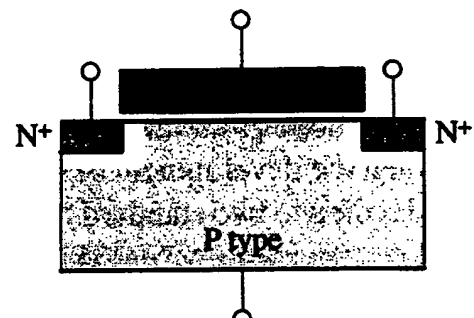
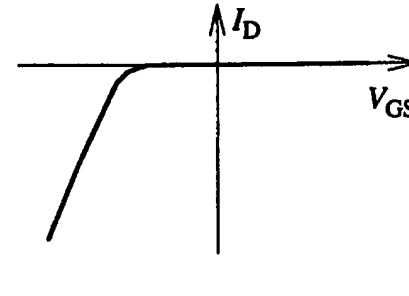
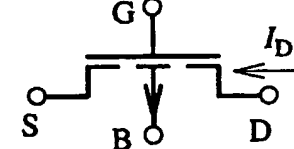
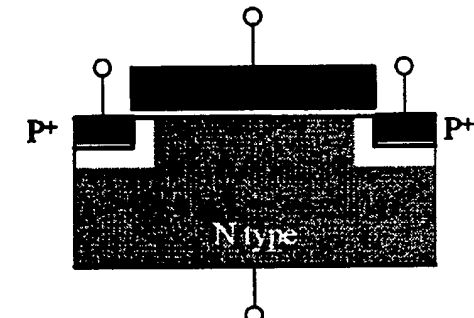
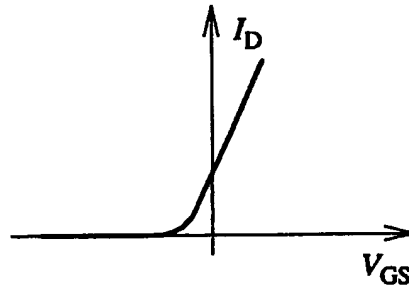
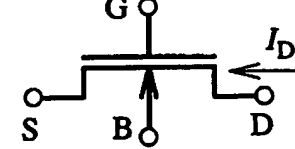
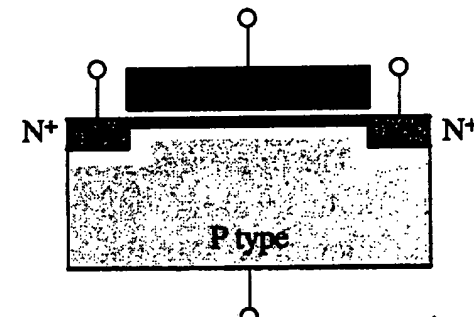
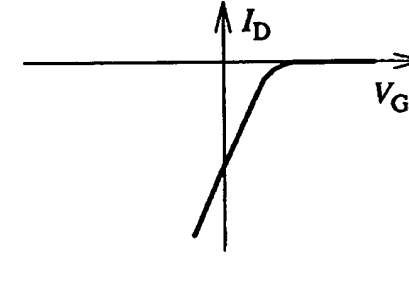
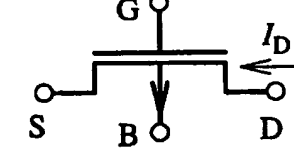
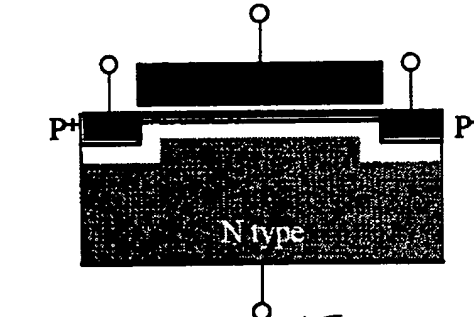


FIG. 5

	N-channel (NMOS)	P-channel (PMOS)
Enhancement (normally off)	   <p>FIG. 6a</p>	   <p>FIG. 6b</p>
Depletion (normally on)	   <p>FIG. 6c</p>	   <p>FIG. 6d</p>

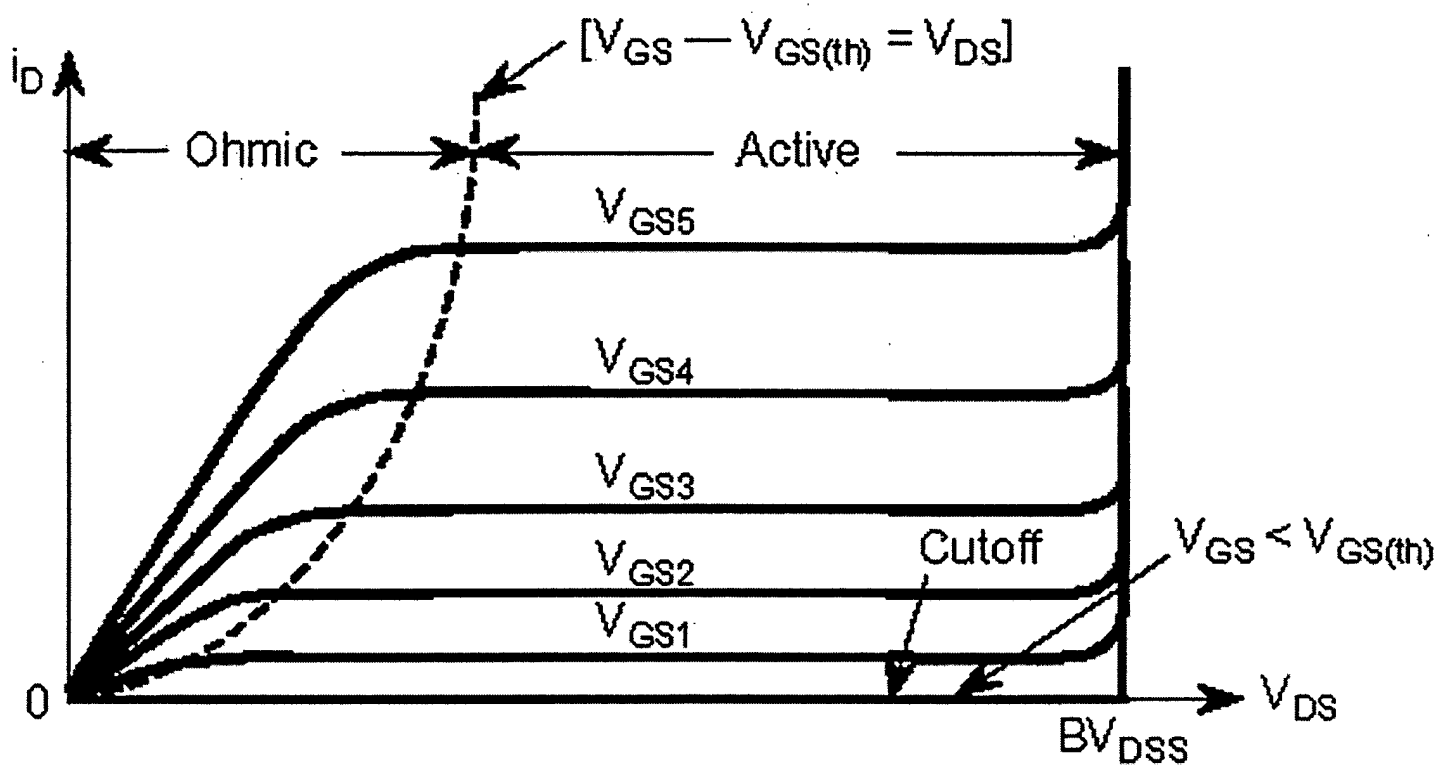


FIG. 7

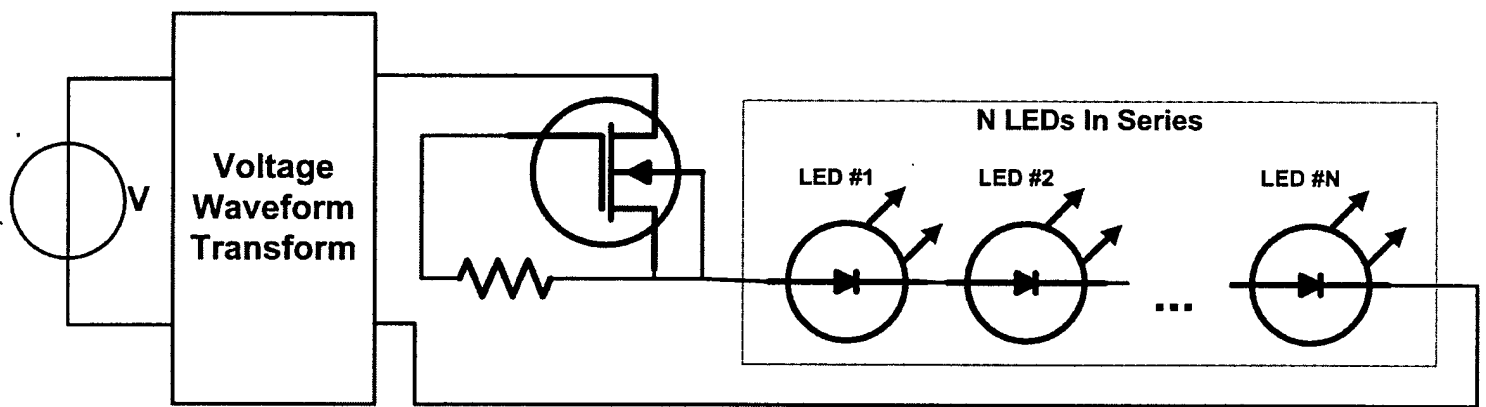


FIG. 8

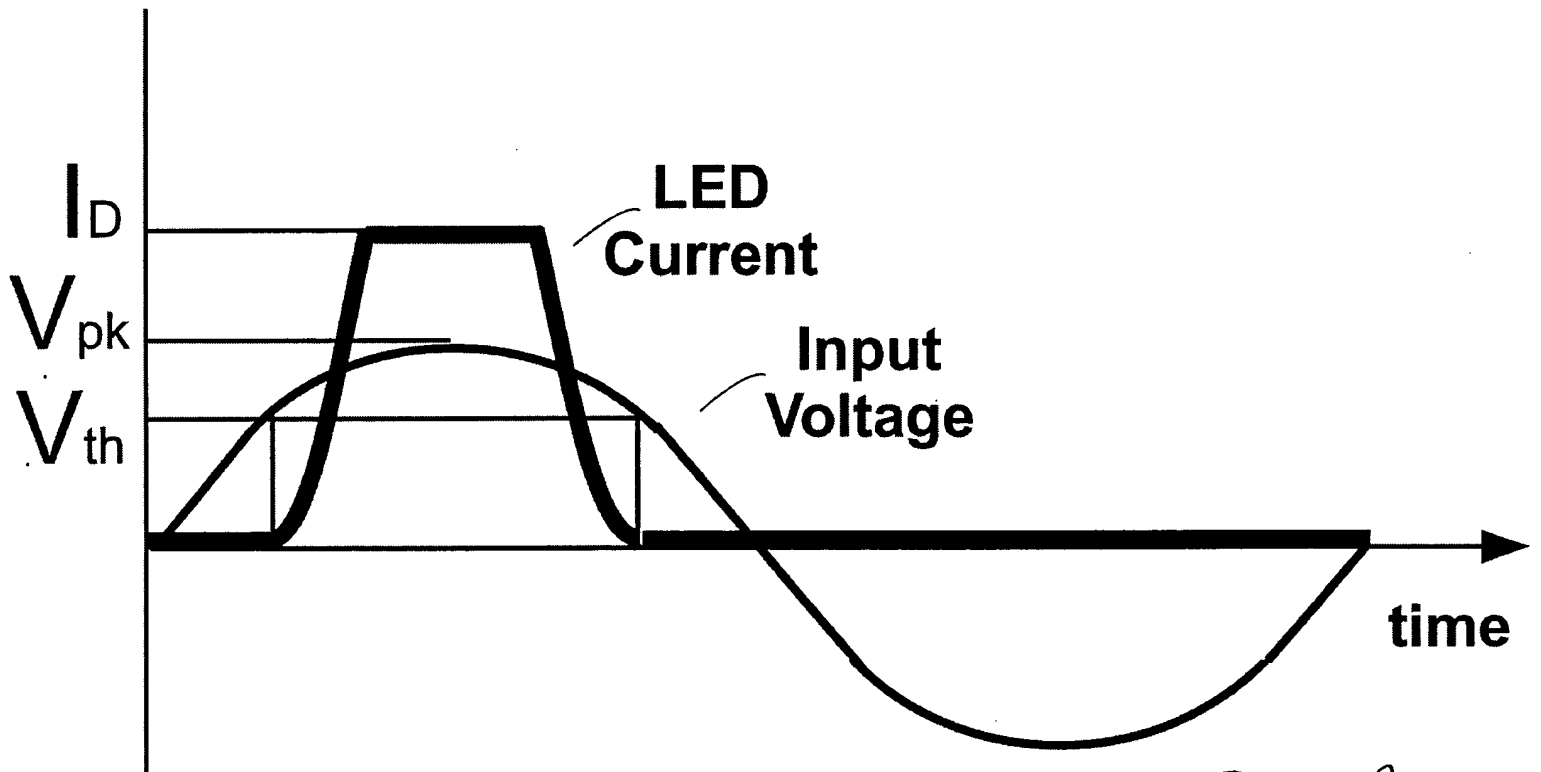


FIG. 9

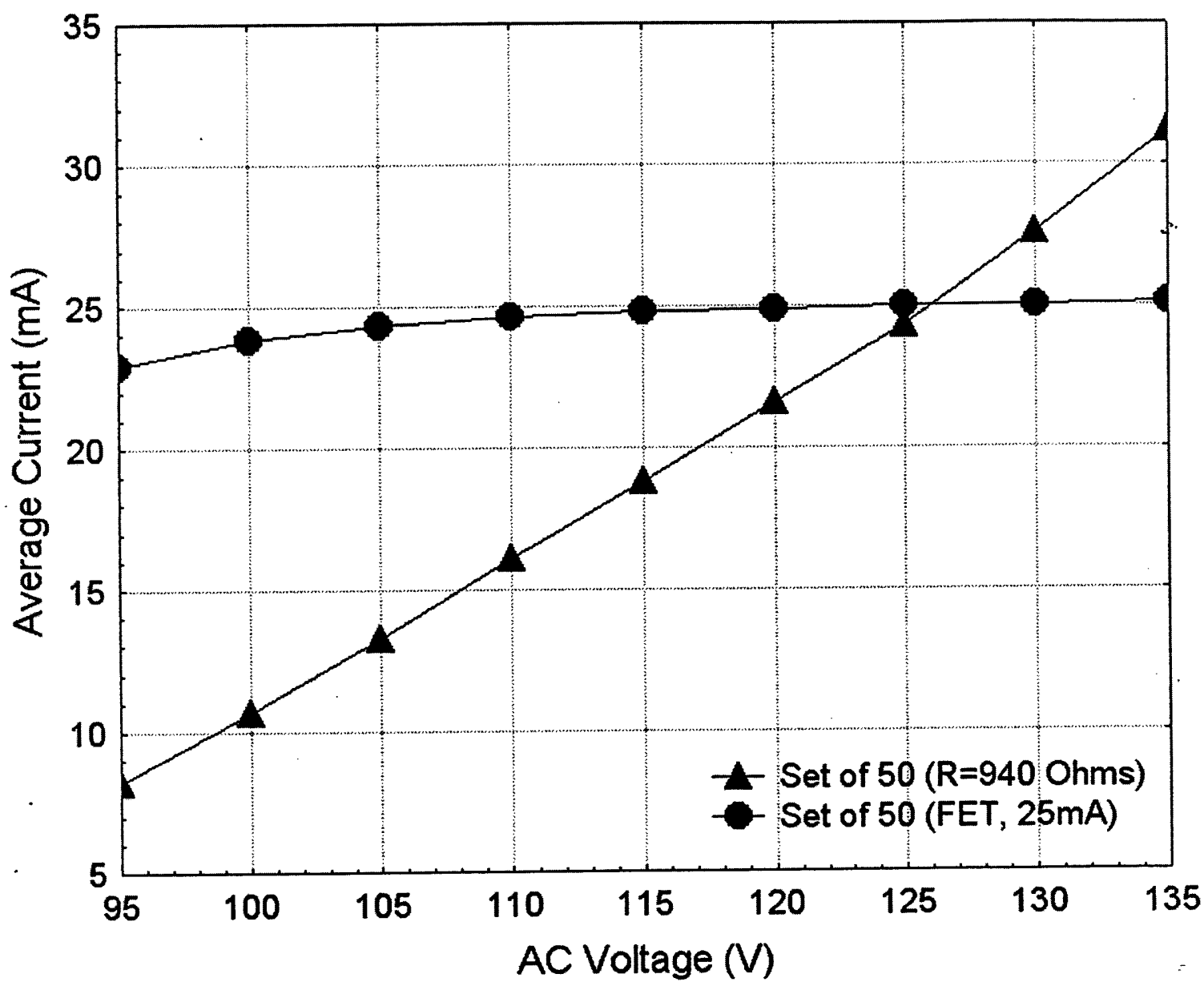


FIG. 10

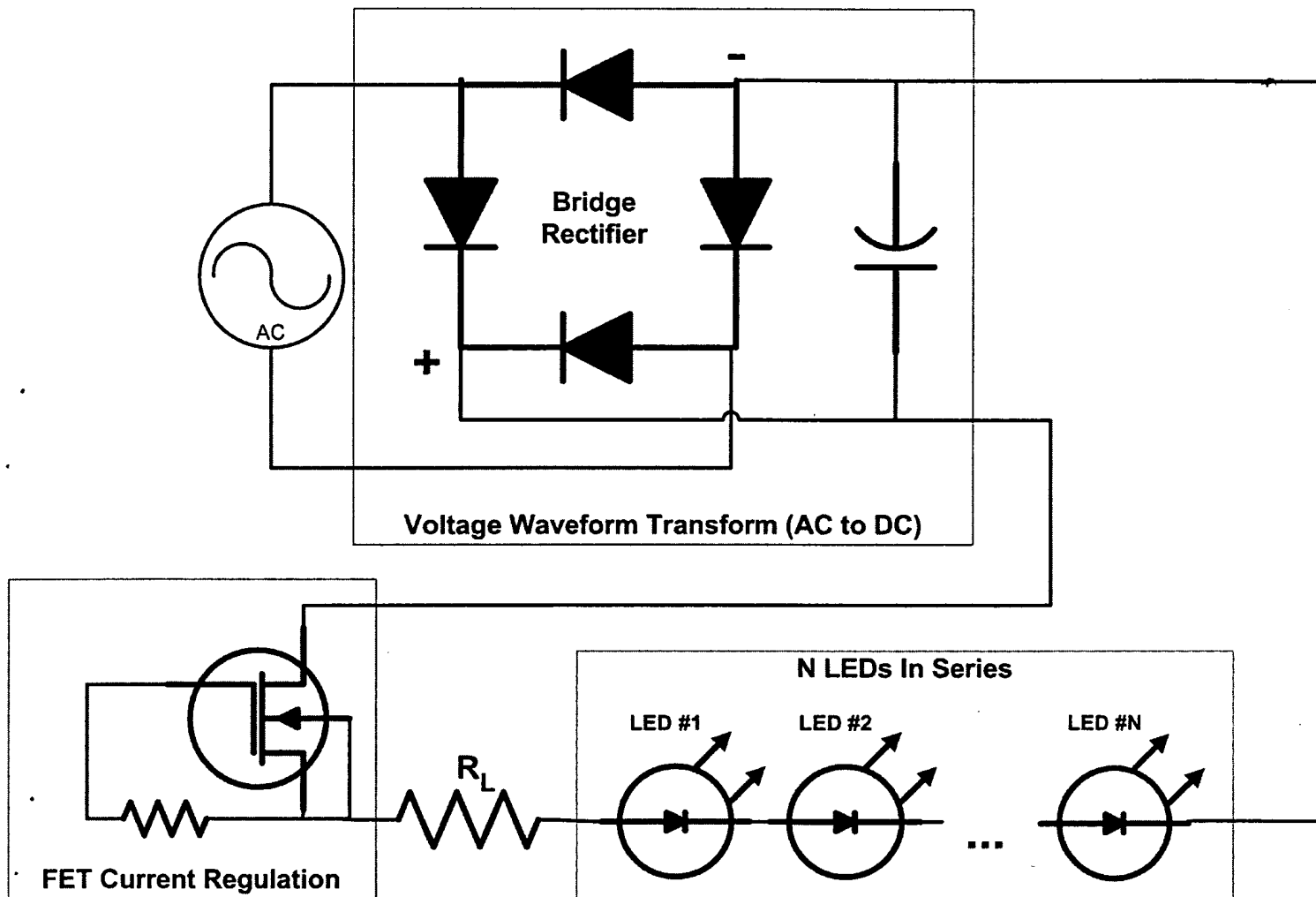


FIG. 11